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Design of the elastic buffer size for SDH equipments

Shi Fuping, Lin Xiankang, Feng Chengxi
Communication Technology Proceedings, 1996. ICCT'96., 1996
International Conference on
Volume: 2
Digital Object Identifier: 10.1109/ICCT.1996.545002
Publication Year: 1996, Page(s): 900 - 904 vol 2
IEEE CONFERENCES

Defending embedded systems against buffer overflow via hardware/software

Shao, Z.; Zhuge, C.; He, Y.; Sha, E.H.-M.;
Computer Security Applications Conference, 2003.
Proceedings. 19th Annual
Digital Object Identifier: 10.1109/CSAC.2003.1284240
Publication Year: 2003, Page(s): 362 - 361
IEEE CONFERENCES

Predictor-directed stream buffers

Shenwood, T.; Sair, E.; Calder, B.;
Microarchitecture, 2000. MICRO 30. Proceedings, 33rd Annual
IEEE/ACM International Symposium on
Digital Object Identifier: 10.1109/MICRO.2000.896067
Publication Year: 2000, Page(s): 42 - 53
IEEE CONFERENCES

The SPEEDS persistence framework and the standard simulation architecture

Steinman, J.S.; Wong, J.W.;
Parallel and Distributed Simulation, 2003. (PADS 2003).
Proceedings. Seventeenth Workshop on
Digital Object Identifier: 10.1109/PADS.2003.1207416
Publication Year: 2003, Page(s): 11 - 20
IEEE CONFERENCES

A decoupled predictor-directed stream preloading architecture

Sair, E.; Shenwood, T.; Calder, B.;
Computers, IEEE Transactions on
Volume: 52, Issue: 3
Digital Object Identifier: 10.1109/TC.2003.1180943
Publication Year: 2003, Page(s): 260 - 276
IEEE JOURNALS

A 46-MHz DSP-augmented RAMDAC for smooth-shaded graphic applications

Leonard, J.; Weite, N.; Eddeny, L.; Horston, S.; Masney, R.;
Solid State Circuits, IEEE Journal of
Volume: 26, Issue: 3
Digital Object Identifier: 10.1109/4.74933
Publication Year: 1991, Page(s): 217 - 226
IEEE JOURNALS

ROSS: a high-performance, low memory, modular time warp system

Carothers, C.D.; Bauer, D.; Pearce, S.;
Parallel and Distributed Simulation, 2000. PADS 2000
Proceedings. Fourteenth Workshop on
Digital Object Identifier: 10.1109/PADS.2000.847144
Publication Year: 2000, Page(s): 82 - 90

IEEE CONFERENCES

Three extensions to register integration

Petro, V.; Bracy, A.; Polin, A.;
 Microarchitecture, 2002. (MICRO-35) Proceedings. 36th Annual IEEE/ACM International Symposium on
 Digital Object Identifier: 10.1109/MICRO.2002.1176237
 Publication Year: 2002, Page(s): 37 - 47

IEEE CONFERENCES

Dead-block prediction & dead-block correlating prefetchers

An-Chow Lai; Rida, G.; Falesti, B.;
 Computer Architecture, 2001. Proceedings. 28th Annual International Symposium on
 Digital Object Identifier: 10.1109/ISCA.2001.937443
 Publication Year: 2001, Page(s): 144 - 154

IEEE CONFERENCES

Interprocessor communication in synchronous multiprocessor digital signal processing chips

Decaluwe, J.; Flabey, J.M.; Van Meerbergen, J.L.; De Man, H.J.;
 Acoustics, Speech and Signal Processing, IEEE Transactions on
 Volume: 37, Issue: 12
 Digital Object Identifier: 10.1109/29.45530
 Publication Year: 1989, Page(s): 1816 - 1826

IEEE JOURNALS

A hardware sort-merge system

Taxagi, N.; Wong, C.-K.;
 IBM Journal of Research and Development
 Volume: 29, Issue: 1
 Digital Object Identifier: 10.1147/rd.291.0049
 Publication Year: 1985, Page(s): 49 - 67

IBM JOURNALS

Don't Use the Page Number, but a Pointer to It

Szemer, S.
 Computer Architecture, 1996. 23rd Annual International Symposium on
 Digital Object Identifier: 10.1109/ISCA.1996.100281
 Publication Year: 1996, Page(s): 104

IEEE CONFERENCES

A scalable coherent cache system with a dynamic pointing scheme

Michael, W.
 Supercomputing '92. Proceedings
 Digital Object Identifier: 10.1109/SUPER.1992.236667
 Publication Year: 1992, Page(s): 358 - 367

IEEE CONFERENCES

Software-extended coherent shared memory: performance and cost

Chakran, D.; Agarwal, A.;
 Computer Architecture, 1994., Proceedings the 21st Annual International Symposium on
 Digital Object Identifier: 10.1109/ISCA.1994.288139
 Publication Year: 1994, Page(s): 314 - 324

IEEE CONFERENCES

Cache invalidation patterns in shared-memory multiprocessors

Gupta, A.; Weber, W.D.;
 Computers, IEEE Transactions on
 Volume: 41, Issue: 7
 Digital Object Identifier: 10.1109/12.256449
 Publication Year: 1992, Page(s): 794 - 810

IEEE JOURNALS

A superassociative tagged cache coherence directory

Lalja, C.J., Ambalavanan, S.,

Computer Design, VLSI in Computers and Processors, 1994

ICCD '94: Proceedings., IEEE International Conference on

Digital Object Identifier: 10.1109/ICCD.1994.331951

Publication Year: 1994 Pages: 42 - 45

IEEE CONFERENCE

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